

Resource Persons

Day-1 to Day -3

Time: 10.00 AM to 12.00 PM & 6.00 PM to 8.00 PM
PATENT DRAFTING AND FILING



Dr. A. Balaji Ganesh, PhD.,

Dean (Research),
Velammal Engineering College,
Chennai.

Director - Founder, IP EVER LLP.

Director - Co Founder,
Emsensing Technologies Private Limited.

Day-4

Time: 10.00 AM to 12.00 PM & 6.00 PM to 8.00 PM
Research Paper Writing



Dr. Varun G Menon

Professor,
International Collaborations &
Corporate Relations - in charge,
Head, Department of CSE,
SCMS School of
Engineering and Technology,
Ernakulam - 683 576, Kerala

Day-5

Time: 10.00 AM to 12.00 PM & 2.00 PM to 3.30 PM
Proposal Writing



Dr. Mainak Adhikari

Assistant Professor
Department of Computer Science
IIIT, Lucknow

Eligibility to Participate

The faculty members of the UGC / AICTE recognized Universities &
Engineering Colleges, Research scholars, and Industry Persons.

Fee Registration

FDP Registration form

<https://forms.gle/GkmrKzYjPup9HEWZ9>



Chief Patron

Dr. N. Vijaya Bhaskara Choudary, Ph.D.,

Secretary & Correspondent

Patron

Mrs. Keerthi Nadella

Executive Director

Program Chair

Dr. C. Yuvaraj

Principal

Co-Chairs

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TECHNOLOGY & SCIENCE**
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Madanapalle-517325, Annamayya Dist., Andhra Pradesh



Department of
Computer Science & Technology

Organizing

Five – Days Online

Faculty Development Programme

On

**“Research Paper writing,
Proposal drafting, and Patenting Strategies”**

From

22-05-2023 to 26-05-2023

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About the Program

The faculty development program is focused on developing skills related to patent drafting, patent filing, Research proposal writing, and Research paper writing. This program is designed to enhance the expertise of faculty members, enabling them to better support the research activities of their students and engage in their own research activities more effectively. Through this FDP, participants will learn how to draft and file patents, as well as how to write effective research proposals and research papers. The program will cover key aspects of these processes, including how to conduct effective research, how to organize and structure research documents, and how to communicate research findings clearly and persuasively. The program will be delivered through a combination of expert lectures and interactive sessions. Participants will have the opportunity to work on their own patent drafts, research proposals, and research papers, receiving feedback and guidance from expert instructors. Overall, this FDP aims to equip faculty members with the skills and knowledge needed to successfully conduct and support research activities in their fields. By participating in this program, faculty members will be better prepared to make significant contributions to their fields and to support the growth and success of their institutions.

About the College

Madanapalle Institute of Technology & Science is being established under the auspices of Ratakonda Ranga Reddy Educational Academy, established in the year 1998, under the visionary and proactive leadership of Dr. N. Vijaya Bhaskar Choudary, Secretary & Correspondent.

MITS is the most sought premier institution, situated in the charismatic silhouette of Horsley Hills, in the hilly regions of Madanapalle, Annamayya district of Andhra Pradesh, adjudged by ISTE as one of the best Engineering colleges.

MITS has grown over the years to become one of the premier institutions in Andhra Pradesh that students aspire to join. Governed by the progressive thoughts of the Management, MITS is always striving conscientiously to excel in various domains of Academics, Research, Contribution to Society by fostering collaborations with Global Academic and Industry. An excellent Academic atmosphere and hi-tech facilities are created for the Faculty and Students for pursuing their professional career and personal goals.



About the Department

The Department of Computer Science & Technology (CST) was established in the year 2018 and plays a vital role in producing value-based professionals to cater to the ever challenging needs of technical excellence in the emerging areas of CST. The department offers one UG program with an intake of 60 students and the intake was enhanced to 180 seats in the year 2019. Department has adequate infrastructural facilities required for imparting high quality education and the department is fully structured to meet the contemporary needs of the industry. Imparting high quality education is supported by well qualified and experienced faculty members with rich academic and industry exposure, who have pursued Masters/Ph.D degree from prestigious institutions like NITs, IITs, and Central Universities within India and abroad. Seminars, Workshops, and Technical Symposia are conducted in the department to keep faculty and students updated with latest developments in various technologies.

Fee Registration

Registration Fee: Rs. 200/- for each participant

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Mr.V.NAVEEN,

Assistant Professor

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Scan the QR code to pay

