



MITS

MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE
(Deemed to be University under section 3 of UGC Act, 1956)

CIRCULAR

Date: 06.01.2026
Madanapalle

All the M. Tech (VLSI & Embedded Systems) I Year I Semester (R25) Regular students are hereby informed that the Practical End Semester Examination will be conducted as per the time table given below.

Time Table

Course Name & Code: CMOS Digital IC Design Laboratory-25MBVESLC01

Date	Branch/Section	Time	Roll No.S	Venue	No.of Students
19.01.2026	M.Tech (VES)	09.30 AM to 12.30 PM	25MRB02001 - 25MRB02022	LB-103	22

Course Name & Code: Advanced Microcontrollers and Signal Processors Laboratory - 25MBVESLC02

Date	Branch/Section	Time	Roll No.S	Venue	No.of Students
20.01.2026	M.Tech (VES)	09.30 AM to 12.30 PM	25MRB02001 - 25MRB02022	LB-103	22

Course Name & Code: Scientific Computing -25MBVESSC01

Date	Branch/Section	Time	Roll No.S	Venue	No.of Students
21.01.2026	M.Tech (VES)	09.30 AM to 12.30 PM	25MRB02001 - 25MRB02022	LB-103	22

LB-Lakshmi Block

Controller of Examinations (I/c)

CONTROLLER OF EXAMINATIONS
Madanapalle Institute of Technology & Science
(Deemed to be University)
MADANAPALLE - 517 325, A. P.