



**MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE
(UCG-AUTONOMOUS)**

Affiliated to JNTUA, Ananthapuramu & Approved by AICTE, New Delhi
Recognized Research Center, Accredited by NBA, NAAC for CSE, ECE, EEE, ME & MBA
World Bank Funded Institute, Recognized by UGC under the sections 2(f) and 12(B) of the UGC act
Recognized as Scientific & Industrial Research Organization by DSIR of DST
Department of Electronics & Communication Engineering

Date: 12/12/2020

Circular

A meeting of Department Advisory Board (DAB) of Electronics and Communication Engineering Department will be held on 26/12/2020 at 11.00 A.M in the board room. All the members of the DAB are requested to kindly attend the same.

Agenda:

The meeting is scheduled to discuss the following:

1. Review of previous meeting
2. Discussion on Vision, Mission and PEOs & feedback received from the faculty
3. Analyzing the report of the Program Assessment Committee and monitoring the progress of the program
4. Modification in Course structure
5. Department academic calendar execution
6. Result analysis and result improvement measures
7. Any other point with permission from chair person.

HOD/ECE

Head of the Department
Electronics & Communication Engineering
Madanapalle Institute of Technology & Science
Angali, MADANAPALLE - 517 325
Chittoor, (Dist., A.P)

Copy to:

All members of DAB



**MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE,
(UCG-AUTONOMOUS)**



Affiliated to JNTUA, Ananthapuramu & Approved by AICTE, New Delhi
Recognized Research Center, Accredited by NBA, NAAC for CSE, ECE, EEE, ME & MBA
World Bank Funded Institute, Recognized by UGC under the sections 2(f) and 12(B) of the UGC act 1956
Recognized as Scientific & Industrial Research Organization by DSIR of DST
Department of Electronics & Communication Engineering

Date: 27/12/2020

Minutes of Meeting for Department Advisory Board

The Department Advisory Board meeting was held on 26-12-2019 in the ECE department. The meeting was presided over by Chairperson Dr.S.Rajasekaran. He extended a warm welcome to the Department Advisory Board members and confirmed the minutes of previous meeting.

Chairman discussed the following agendas Department Vision , Mission and PEOs of the program. Attainment of POs , PSOs and Quality objectives Program effectiveness Changes / revision needed (if any)

The following modifications in Course structure were discussed in the meeting (changes from R-14 regulation to R-18 regulation)

In III Yr I Sem, Professional Course subjects are reduced from 4 to 2. Electromagnetic theory is removed. Communication System is changed to Analog & Digital Communication. Analog Electronics and Analog & Digital VLSI Design subjects are removed. Digital Signal Processing is introduced. Open elective Computer Architecture is removed and changed to both open and discipline elective. Analog Electronics Practicals is changed to AI Tools, Techniques & Applications Laboratory Communication System Practicals is changed to Analog and Digital Communication Laboratory. Digital Signal Processing laboratory is introduced. Accordingly syllabus is changed.

In III Yr II Sem, Electromagnetic fields and Microwave Engineering is Changed to Electromagnetic waves and Waveguides. Communication Networks and Digital Signal Processing subjects are removed. VLSI Design is introduced. Microwave and Digital Signal Processing Practicals are changed to IOT and Virtual Laboratory. Accordingly syllabus is changed. The modifications in Course structure is approved by Department Advisory Board (DAB).

Change in Course structure from R-14 regulation to R-18 regulation and percentage of deviation is shown in the table for III Yr I & II Sem.



MADANAPALLE INSTITUTE OF TECHNOLOGY
(UGC-AUTONOMOUS)



Affiliated to JNTUA, Anantapuramu & Approved by
Recognized Research Center, Accredited by NBA, NAAC for C
World Bank Funded Institute, Recognized by UGC under the sections 2(f) and 12(B) of the UGC act 1956
Recognized as Scientific & Industrial Research Organization by DSIR of DST
Department of Electronics & Communication Engineering

Comparison of B.Tech.(Regular) R14 and R18 Regulations

B.Tech.(Regular) R14 Regulations		B.Tech.(Regular) R18 Regulations III- I Semester		B.Tech.(Regular) R18 Regulations III- II Semester		
B. Tech III- I Semester Course Code	Course Name	B. Tech III- II Semester Course Code	Course Name	Course Code	Course Name	% Change
14ECE109	Electromagnetic Theory	14ENG103	Soft Skills	18ENG102	English Communication - Reading and Writing	100
14ECE110	Communication Systems	14ECE114	Communication Networks	18ECE108	Artificial Intelligence Tools, Techniques and Applications	100
14ECE111	Analog Electronics	14ECE115	Electromagnetic Fields and Microwave Engineering	18ECE109	Analog and Digital Communication	40
14ECE112	Analog and Digital VLSI Design	14ECE116	Digital Signal Processing	18ECE110	Digital Signal Processing	100
14ECE113	Computer Architecture	14ECE401	DE-Optical Communication			
	Open Elective - I	14ECE402	DE-Digital Processing			
14ECE205	Analog Electronics Practicals	14ECE403	DE-Electronics Measurements Instrumentation		Discipline Elective - I	100
					Discipline Elective - II	
					MOOC Course	
					Discipline Elective - III	
					Discipline Elective - III 1. 18ECE406 Fiber Optic Communication	40



**MADANAPALLE INSTITUTE OF TECHNOLOGY
(UGC-AUTONOMOUS)**



new Delhi

Affiliated to JNTUA, Ananthapuram & Approved by
 Recognized Research Center, Accredited by NBA, NAAC for C
 World Bank Funded Institute, Recognized by UGC under the sections 2(f) and 12(B) of the UGC act 1956

Department of Electronics & Communication Engineering

						1. 18ECE401 Electronics 2. 18ECE402 Pattern Recognition and its Applications 3. 18ECE403 Bio-Medical Electronics 4. 18ECE404 Electronics Packaging and Testing 5. 18ECE405 Networks and Transmission Lines	100					2. 18ECE407 Embedded Systems 3. 18ECE408 Information Theory and Coding 4. 18ECE409 FPGA based System Design 5. 18ECE410 DSP Architectures	100	
14ECE206	Communication Systems Practicals		Open Elective - II											
		14ECE207	Microwave Practicals											
		14ECE208	Digital Signal Processing Practicals											
					Open Elective - I							Open Elective - II		
					Artificial Intelligence Tools, Techniques and Applications Laboratory	100						Corporate Communication Laboratory	100	
					Analog and Digital Communication Laboratory	20						Internet of Things Laboratory	100	
					Digital Signal Processing Laboratory	100								
					Mandatory Course - III							Mandatory Course - IV		
					Total Percentage Change = 1900/72 = 27 %									



MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE
(UCG-AUTONOMOUS)



Affiliated to JNTUA, Ananthapuramu & Approved by AICTE, New Delhi
Recognized Research Center, Accredited by NBA, NAAC for CSE, ECE, EEE, ME & MBA
World Bank Funded Institute, Recognized by UGC under the sections 2(f) and 12(B) of the UGC act 1956
Recognized as Scientific & Industrial Research Organization by DSIR of DST
Department of Electronics & Communication Engineering

- CO-PO mapping is reviewed as per rubrics and DAB approved the same.
- Chairperson asked suggestion to improve Course Outcomes (CO), Program Outcomes (PO), Program Specific Outcomes (PSO) attainment from members.
- Members reviewed stakeholder's feedback for the academic year 2019-20 and recommended to prepare the action taken report for continuous improvements.
- Secretary discussed the academic year 2019-2020 semester result analysis and department quality objectives and its outcomes.
- Members suggested to conduct workshops on Embedded systems & VLSI

The following faculty members attended the meeting.

S.NO	Name of the Faculty	Designation	Position
1	Dr.S.Rajasekaran	Professor &HOD	Chairman <i>try</i>
2	Dr.P.Ramanathan	Professor	Secretary
3	Dr.Kumar Manoj	Professor	Internal Member <i>Manoj</i>
4	Dr.Brijesh Kumar Singh	Professor	Internal Member <i>Brijesh</i>
5	Dr.Satrugan Kumar	Professor	Internal Member <i>Satrugan</i>
6	Dr.Veeraiyah Thangasamy	Professor	Internal Member <i>Thangasamy</i>
7	Dr.K.Sathesh	Assoc.Professor	Internal Member <i>Sathesh</i>

HOD/ECE

[Signature]
Head of the Department
Electronics & Communication Engineering
Madanapalle Institute of Technology & Science
Angallu, MADANAPALLE - 517 325
Chittoor (Dist.) A.P