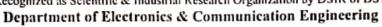
MADANAPALLE INSTITUTE OF TECHMOLOGY & SCIENCE

(UCG-AUTONOMOUS)

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Date: 12/12/2020

Circular

A meeting of Department Advisory Board (DAB) of Electronics and Communication Engineering Department will be held on 26/12/2020 at 11.00 A.M in the board room. All the members of the DAB are requested to kindly attend the same.

Agenda:

The meeting is scheduled to discuss the following:

- 1. Review of previous meeting
- 2. Discussion on Vision, Mission and PEOs & feedback received from the faculty
- Analyzing the report of the Program Assessment Committee and monitoring the progress of the program
- 4. Modification in Course structure
- 5. Department academic calendar execution
- 6. Result analysis and result improvement measures
- 7. Any other point with permission from chair person.

HOD/ECE

Head of the Department
Electronics & Communitation Engineering
Madanapalle Institute of Technology & Science
Angaliu, MADANAPALLE - 517 325
Chittoor (Dist., A.P.

Copy to:

All members of DAB

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Department of Electronics & Communication Engineering

Date: 27/12/2020

Minutes of Meeting for Department Advisory Board

The Department Advisory Board meeting was be held on 26-12-2019 in the ECE department. The meeting was presided over by Chairperson Dr.S.Rajasekaran. He extended a warm welcome to the Department Advisory Board members and confirmed the minutes of previous meeting.

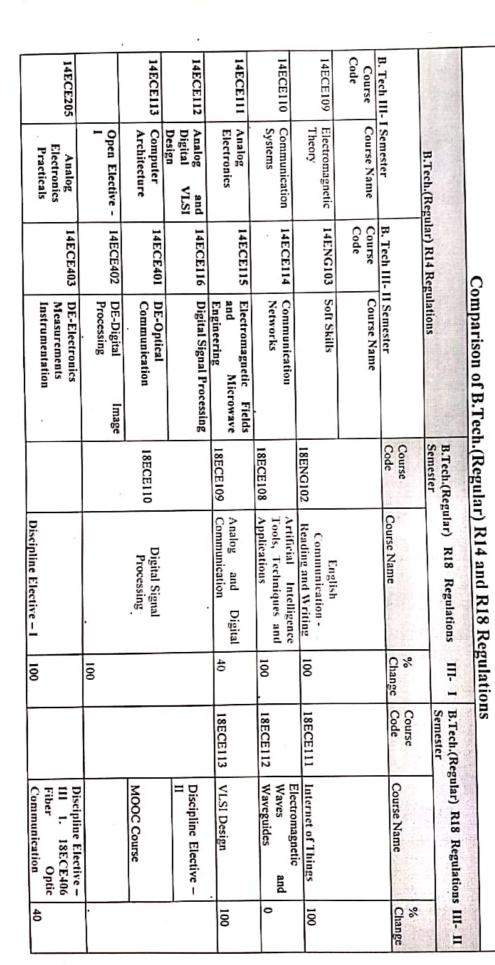
Chairman discussed the following agendas Department Vision, Mission and PEOs of the program. Attainment of POs, PSOs and Quality objectives Program effectiveness Changes / revision needed (if any)

The following modifications in Course structure were discussed in the meeting (changes from R-14 regulation to R-18 regulation)

In III Yr I Sem, Professional Course subjects are reduced from 4 to 2. Electromagnetic theory is removed. Communication System is changed to Analog & Digital Communication. Analog Electronics and Analog & Digital VLSI Design subjects are removed. Digital Signal Processing is introduced. Open elective Computer Architecture is removed and changed to both open and discipline elective. Analog Electronics Practicals is changed to Al Tools, Techniques & Applications Laboratory Communication System Practicals is changed to Analog and Digital Communication Laboratory. Digital Signal Processing laboratory is introduced. Accordingly syllabus is changed.

In III Yr II Sem, Electromagnetic fields and Microwave Engineering is Changed to Electromagnetic waves and Waveguides. Communication Networks and Digital Signal Processing subjects are removed.VLSI Design is introduced. Microwave and Digital Signal Processing Practicals are changed to IOT and Virtual Laboratory. Accordingly syllabus is changed. The modifications in Course structure is approved by Department Advisory Board (DAB).

Change in Course structure from R-14 regulation to R-18 regulation and percentage of deviation is shown in the table for III Yr I & II Sem.





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Department of Electronics & Communication Engineering

Total Percentage Change = 1900/72 = 27 %

Mandatory Course

Mandatory

Course

18ECE207

Laboratory Communication Analog

3

18ECE209

Laboratory

8

Internet of Things

18ECE206

Laboratory

and

Digital

8

18ENG202

Laboratory

100

Communication

Corporate

Applications

Tools, Techniques and Artificial Intelligence Open Elective – I

Open Elective - II

18ECE208

Processing Laboratory

100

Digital

Signal



14ECE206

Communication Systems Practicals

Open Elective - II

1.18ECE401

Nano

Electronics

Audit Course - II

3.18ECE403

Medical Electronics

Design

based

System

5. 18ECE410

DSP

100

Architectures

4.18ECE404

Recognition

ä

7

Information Theory

18ECE408

and Coding

8

Embedded Systems

18ECE407

4. 18ECE409 FPGA

90

Applications

2. 18ECE402 Pattern

14ECE208

Digital Signal Processing

5. 18ECE405 Networks

Transmission

Lines

and Testing

8

Electronics Packaging

Practicals

14ECE207

Microwave Practicals

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Department of Electronics & Communication Engineering

- CO-PO mapping is reviewed as per rubrics and DAB approved the same.
- Chairperson asked suggestion to improve Course Outcomes (CO), Program Outcomes (PO), Program Specific Outcomes (PSO) attainment from members.
- Members reviewed stakeholder's feedback for the academic year 2019-20 and recommended to prepare the action taken report for continuous improvements.
- Secretary discussed the academic year 2019-2020 semester result analysis and department quality objectives and its outcomes.
- Members suggested to conduct workshops on Embedded systems & VLSI

The following faculty members attended the meeting.

S.NO	Name of the Faculty	Designation	Position
1	Dr.S.Rajasekaran	Professor &HOD	Chairman CT
2 .	Dr.P.Ramanathan	Professor	Secretary
3	Dr.Kumar Manoj	Professor .	Internal Member
4	Dr.Brijesh Kumar Singh	Professor	Internal Member
5	Dr.Satrughan Kumar	Professor	Internal Member
6	Dr.Veeraiyah Thangasamy	Professor	Internal Member
7	Dr.K.Sathesh	Assoc.Professor	Internal Member

HODECE

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