



# MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE, MADANAPALLE

(UGC - Autonomous)

(Approved by AICTE, New Delhi & Affiliated to JNTUA, Anantapuramu)  
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## Department of Electrical & Electronics Engineering

### Minutes of 6<sup>th</sup> Board of Studies Meeting Held on 15-06-2019

the 6<sup>th</sup> BoS Meeting is held on 15-06-2019 at 11.30am in the department seminar hall, Dept. of EEE, MITS, Madanapalle.

#### Agenda

1. Discussion on B.Tech. Degree 2018-19 academic year curriculum structure for all the semesters and detailed syllabus for the II year - I and II semesters.
2. Any other matter with the permission of chair

#### Members Present:

1. Dr. Asha Rani M. A.  
HoD, Dept. of EEE, MITS.
2. Prof. P.S. Nagendra Rao,  
Professor, Dept. of EE, IIT, Dharwad
3. Dr. K. Siva Kumar,  
Assoc. Professor, Dept. of EE, IIT, Hyderabad
4. Dr. P. Raja,  
Assoc. Professor, Dept. of EEE, NIT, Tiruchirappalli
5. Ms. B.R. Sushma,  
Scientist, DRDO, Bangalore
6. Dr. M. Ilampoornan,  
Professor, Dept. of EEE, MITS.
7. Dr. C. Kamal Basha,  
Professor, Dept. of EEE, MITS.
8. Dr. Rajendraprasad Narne,  
Assoc. Professor, Dept. of EEE, MITS.
9. Dr. M. Chakkarapani,  
Assoc. Professor, Dept. of EEE, MITS.
10. Dr. Lakshmanan S. A.,  
Sr. Asst. Professor, Dept. of EEE, MITS.

Chairperson



Member - Subject Expert

PS. Nagendra Rao

Member - Subject Expert

K. Siva Kumar

Member - University Nominee

P. Raja

Member - Meritorious Alumnus

B.R. Sushma

Member

M. Ilampoornan

Member

C. Kamal Basha

Member

R. Rajendraprasad Narne

Member

M. Chakkarapani

Member

S. A. Lakshmanan

11. Dr. Hira Singh Sachdev,  
Sr. Asst. Professor, Dept. of EEE, MITS.

Member

*Hira*  
15/06/19

#### A. Suggestions on B. Tech. – Curriculum structure

1. Digital electronics theory and laboratory can be moved from II year I semester to II year II semester and Analog electronics theory and laboratory can be brought in place.
2. The electrical machines course has to be offered as two core courses (Theory and Laboratory) as EM-I (DC machines and transformers) & EM – II (Induction and synchronous machines) instead of providing as a single course.
3. Microcontroller and interfacing theory and laboratory can be shifted from II year II semester to III year I semester.
4. AI tools, techniques and applications theory can be shifted from III year I semester to III year II semester to shift Power system – I theory and laboratory to III year I semester. Also, it is recommended to merge AI and IoT laboratories and offer as a single laboratory in IV year I semester.
5. Measurement and Instrumentation theory and laboratory can be removed from the core courses and can be offered as a discipline elective if needed.
6. Simulation laboratory has to be shifted from III year I semester to IV year I semester.
7. Power system – II can be shifted from IV year I semester to III year II semester.
8. Switchgear and protection course can be brought as a core course and Electric drives and control can be offered as a discipline elective.
9. All the electives can be merged and can be kept as a single discipline elective group for making provision for students to have their own choice.
10. MOOCs courses can be even selected from the open electives/mandatory courses as and when required for satisfying the training requirements. Any minor differences in the course content offered by SWAYAM may be ignored at the discretion of the IDC.

#### B. Suggestions on B.Tech. – II year I & II semester detailed syllabus

1. The syllabus content of the core and discipline elective courses can be appropriately modified for making it consistent if necessary. BoS authorizes the Chairperson and IDC for making appropriate changes.

