MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE, MADANAPALLE
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DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Date: 02.11.2020

Circular

It is hereby informed to all the members of Program Assessment Committee (PAC) that 9th Program Assessment Committee (PAC) Meeting is scheduled on 04-11-2020, Wednesday, 11:00 a.m. at the board room (EB-001), MITS, Madanapalle, for addressing and reviewing the assessment method for attainment of Course Outcomes (COs), Program Outcomes (POs) and Program Specific Outcomes (PSOs).

Agenda:

1. Assessment of previous results and analysis of Course outcomes (COs), Program Outcomes (POs) and Program Specific Outcomes (PSOs), for the academic year 2019-20, I semester subjects.
2. Discussion on assessment methods to achieve attainment level in R-14 Regulation for UG syllabus.
3. Discussion on department vision and mission, if it is in line with institutional vision mission, POs, PEOs and PSOs or not?
4. Discussion on existing R14 courses
5. Discussion on R18, I, II and III year courses.
6. Any other matter with the permission of the chair.

Dr. A V Pavan Kumar.
HoD/EEE

Head of the Department
Electrical & Electronics Engineering
Madanapalle Institute of Technology & Science
MADANAPALLE - 517 325

1. Dr. Pavan Kumar AV
2. Dr. C. Kamal Basha
3. Dr. M Vaigundamoorthi
4. Dr. Rajendraprasad Narne
5. Dr. M. Chakkarapani
6. Dr. M Vijay
7. Dr. Arijit Bardhan Roy
8. Dr. Saran Satsangi

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Department of Electrical & Electronics Engineering

Date: 2.11.2020

Composition and approval of 9th Program Assessment Committee (PAC)

Following members are nominated and approved for constitutions of 9th Program Assessment Committee (PAC).

1. Dr. A V Pavan Kumar — Chairman
   HoD, Dept. of EEE, MITS.
2. Dr. C. Kamal Basha — Member
   Professor, Dept. of EEE, MITS.
3. Dr. M Vaigundamoorthi — Member
   Professor, Dept. of EEE, MITS.
4. Dr. Rajendraprasad Narne — Member
   Assoc. Professor, Dept. of EEE, MITS.
5. Dr. M. Chakkarapani — Member
   Assoc. Professor, Dept. of EEE, MITS.
6. Dr. M Vijay — Member
   Assoc. Professor, Dept. of EEE, MITS.
7. Dr. Arijit Bardhan Roy — Member
   Sr. Asst. Professor, Dept. of EEE, MITS.
8. Dr. Saran Satsangi — Member
   Sr. Asst. Professor, Dept. of EEE, MITS.

Responsibilities of the committee:

1. Monitors attainment of COs, POs and PSOs
2. PAC evaluates programme effectiveness and process necessary changes
3. Preparation of periodic reports, records on program activities, progress and status reports.

HoD/EEE

Head of the Department
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Principal

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Copy to

- The Principal
- Vice Principal Academics
- Programme Assessment Committee
- Department File

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Department of Electrical & Electronics Engineering

Date: 04.11.2020

Minutes of 9th Program Assessment Committee (PAC) Meeting Held on 04-11-2020

In continuation with the previous meeting dated 03rd April, 2019, PAC has been assembled on 4th November, 2020 at 10.30 a.m. at the board room (EB-001), MITS, Madanapalle, for addressing and reviewing the Assessment method for attainment of Course Outcomes (COs), Program Outcomes (POs) and Program Specific Outcomes (PSOs).

Agenda:

1. Assessment of previous results and analysis of Course outcomes (COs), Program Outcomes (POs) and Program Specific Outcomes (PSOs), for the academic year 2018-19, II semester and 2019-20, I semester subjects.
2. Discussion on assessment methods to achieve attainment level in R-14 Regulation for UG syllabus.
3. Discussion on department vision and mission, if it is in line with institutional vision mission, POs, PEOs and PSOs or not?
4. Discussion on existing R14 courses
5. Discussion on R18, III and IV year courses.
6. Any other matter with the permission of the chair.

Members Present:

1. Dr. A V Pavan Kumar
   HoD, Dept. of EEE, MITS. — Chairman
2. Dr. C. Kamal Basha
   Professor, Dept. of EEE, MITS. — Member
3. Dr. M Vaigundamoorthi
   Professor, Dept. of EEE, MITS. — Member
4. Dr. Rajendraprasad Narne
   Assoc. Professor, Dept. of EEE, MITS. — Member
5. Dr. M. Chakkarapani
   Assoc. Professor, Dept. of EEE, MITS. — Member
6. Dr. M Vijay
   Assoc. Professor, Dept. of EEE, MITS. — Member
7. Dr. Arijit Bardhan Roy
   Sr. Asst. Professor, Dept. of EEE, MITS. — Member

HoD, welcomed the members of the committee who had assembled for reviewing the assessment method of Course Outcomes (COs), Program Outcomes (POs) and Program Specific Outcomes (PSOs) for the EEE department.

The following points were discussed during the meeting and the minutes were recorded as below,

AY:2018-19

A. B.Tech. – II year II semester

1. The attainments of CO_1 and CO_2 for the course Network Analysis are 47% and 56% respectively. Attainments of all other COs are more than 60%. The committee members have suggested for remedial classes focused on practice of network analysis numerical to attain the target of CO_1. The committee members have suggested that the attainment of CO_2 can be improved by giving more assignment problems and solving more numerical in the class.

2. The attainment of CO_1 and CO_5 for the course Electrical Machines are 53% and 56% respectively. Attainments of all other COs are more than the target level. The committee members have suggested that the attainment of CO_1 can be achieved to the targeted level through practical demonstration of cut-sections of machines and animated videos on working principle of machines. As per the suggestion of the committee members, the attainment of CO_5 can reach to the target level by solving more numerical focused on power flow equation of synchronous machines.

3. In the course Digital Design the attainment of CO_1, CO_2, CO_3 and CO_5 are 53%, 51%, 57% and 59% respectively. Attainments of all other COs have reached the target level. The committee members have suggested giving separate assignment for that particular unit can help to reach the attainment of CO_1. The committee members have suggested solving more numerical in class can help in attaining the target of CO_2 and CO_3. The committee members also suggested remedial classes to attain the target level of CO_5.

4. The attainment of all COs (CO1-CO5) in the course are attained. So, the committee members have suggested increasing the target value from 60 % to 65% for the particular course which have 70% and above attained.

B. B.Tech. – III year II semester

1. The attainment of CO1, CO2 and CO3 for the course Digital Signal Processing is 48%, 47% and 54%. Attainments of all other COs are more than target value. The committee members have suggested that, the attainment of CO1, CO2 and CO3 can be enhanced by practical on the different types of bridges and remedial classes for week students focusing on more practice of numerical.
2. In the course Signals & Systems the attainment of CO2, CO4 and CO5 are 54%, 59% and 51% respectively. The attainment of the CO1 and CO3 have achieved the target value. The committee members have been suggested to solve more numerical problems in class and remedial classes for identified week students to enhance the attainment of CO2, CO3, CO4 and CO5. The attainment compared to the previous AY only CO3 has improved. The committee has advice to make changes in the teaching learning methodology.

Head of the Department

AY: 2019-20

C. B.Tech. – II year I semester

1. The attainment of CO2, CO3, CO4 and CO5 for the course Analog Electronics is 53%, 41%, 56% and 59%. Attainments of all other CO1 are more than 60%. The committee members have suggested solving numerical in the class to improve the attainment of COs. Modify the content delivery methods to make the concepts slear by taking real time examples of the hardware systems.

D. B. Tech. – III year I Semester

1. The attainment of both CO5 for Power Electronics course is just below the target level i.e. 54%. Attainments of all other COs are more than 60%. The committee members have been suggested to give more assignment problems to increase the attainments of CO5.

2. The attainment of CO3 for the course Switchgear & Protection is below the target value i.e. 51%. Attainments of all other COs are more than the target level. The committee members have suggested that the attainment of CO1 can be enhanced by practical demonstration, animated videos and by solving more numerical in the class.

E. Attainment of POs and PSOs for B.Tech: 2015-2019 Batch (Second Autonomous Batch)

The attainment of all the POs (PO1-PO7) is level 1, (PO8 – PO12) is level 2 and PSOs (PSO1, PSO3) is level 1 and PSO2 is level 2 for the B.Tech batch of 2015-2019. All POs and PSOs have been successfully attained above level 1. The committee members have suggested that the target value of POs and PSOs can be set to level 2 for the 2016-2020 B.Tech batch with the approval of BoS committee.

F. Guest Lecture and Workshop

The committee members have suggested that the guest lectures on the industrial application and on the importance of the subjects like network analysis, digital design, electronics devices and signals & systems can improve the attainment of the COs. The committee members have also suggested to conduct workshops and hand-on training for industrial tuning of the subjects like Measurement & Instrumentation, Switchgear Protection, Electrical Machines and Electric Power Utilization to achieve COs more than the target level.

G. The Committee members have not suggested any modification in the department vision & mission as it is in line with the institutional vision & mission, POs, PEOs and PSOs.

H. Committee members have discussed about R18 III year courses.

PAC Chairperson
HOD/EEE

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