

**MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE, MADANAPALLE  
(UGC-AUTONOMOUS)**

**M.Tech - VLSI & Embedded Systems**


**Results - Seminar & Project Viva Voce Examination**

S.No	Roll No	Project Work - (14VES22D01)		Seminar - (14VES22S01)		Month Year of Passing
		L.G	C	L.G	C	
1	14691D6812	A+	16	O	2	Dec 2017
2	15691D6807	A+	16	O	2	Dec 2017
3	15691D6809	A+	16	O	2	Dec 2017

Date: 05.01.2018

  
Controller of Examinations  
**CONTROLLER OF EXAMINATIONS**  
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