



Schedule for Postponed B.Tech. IV Year I Semester I Mid –Term Test November – 2021

EXAM SECTION – NOTIFICATION DATED : 23.11.2021

B.Tech. IV Year I Semester (2018 Admitted Batch)

Academic Year – 2021 - 22

TIMINGS: AN: 03:00 PM to 04:30 PM

DATE / DAY	Civil Engineering (CE)	Electrical and Electronics Engineering (EEE)	Mechanical Engineering (ME)	Electronics and Communication Engineering (ECE)	Computer Science and Engineering (CSE)	Computer Science and Information Technology (CSIT)	Computer Science and Technology (CST)
27.11.2021 (Saturday)	Introduction to Research (18IE3M01)	Power Plant Engineering (18ME3M04)	Waste Water Treatment and Recycling (18CE3M05)	Multi-Core Computer Architecture – Storage and Interconnects (18CSE3M05)	Management Information Systems (18HUM3M03)	Management Information Systems (18HUM3M03)	Management Information Systems (18HUM3M03)


Controller of Examinations
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PRINCIPAL
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MADANAPALLE

1. All HODs : CE EEE ME ECE CSE CST

EC To:

1. SAO(G) SAO (A&P) S.P.O AO(Systems) SWO Dean(R&D) Dean(PGP) TC Librarian SO TI Dy.Warden
2. Copy to HOD's to be read in B.Tech IV Year I Semester Class Rooms
3. Department & Academic Section Notice Boards