MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE, MADANAPALLE- 517 325

(Deemed to be University)

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

TIME TABLE - AY :: 2026-27 - II B. TECH- I SEMESTER

Class Teacher: Dr Vijaya Bhaskar Somu **W.E.F: 15/07/2026 (Revision:1)** **Room No:KKB 009/010**

DAY/TIME	9:10-10:10AM	10:10-11:10AM	11:10-12:10PM	12:10-1.00PM	1.00-2.00PM	2.00-3.00PM	3.00-4.00PM	4.00-5.00PM
MON	T & NM	ECA	DSD	LUNCH	AE	DCMT Lab		
TUE	DSD	T & NM	DCMT		Aptitude	ECA	Holistic Activities	
WED	AE	DSD	DCMT		T & NM	Mentoring	Holistic Activities	
THU	AE	ECA	DCMT		Digital Systems Lab		NSDC	
FRI	ST for EE	ST for EE			Analog Electronic Circuits Lab		Library	
SAT								

THEORY			
BSC	25BAMATTC07	Transforms and Numerical Methods	Dr A Subba Rao
ESC	25BAEEETC04	Electric Circuit Analysis	Dr Manavalan G
PCC	25BAEEETC05	DC Machines and Transformers	Dr Kamal Basha C
PCC	25BAEEETC06	Analog Electronic Circuits	Dr Vijaya Bhaskar Somu
PCC	25BAEEETC07	Digital System Design	Dr Vineet Kumar
SEC - I	25BAEEESC01	Simulation Tools for Electrical Engineers	Dr Manavalan G
		Aptitude	Mr. Shaik Tipu Rahaman
LAB			
PCC	25BAEEELC02	DC Machines and Transformers Lab (SRB 012C)	Dr R Saravanakumar
PCC	25BAEEELC03	Analog Electronics Circuits Laboratory (SRB 119)	Dr Vijaya Bhaskar Somu
PCC	25BAEEELC04	Digital Systems Laboratory (SRB 119)	Dr Vineet Kumar
SEC - I	25BAEEESC01	Simulation Tools for Electrical Engineers (Siemens lab LB 019)	Dr Satish Kumar Ramoji & Dr Manavalan G
EEC		NSDC - Certification Course – I	Dr Vijaya Bhaskar Ramu

Time Table Incharge **HoD/EEE** **Registrar**