

Report on Guest Lecture
“VLSI Design and Applications”
Organised by
Department of Electronics and Communication Engineering
06-03-2017

Organized in association with: MITSAWA

Submitted by: Mr. K. Rahim Basha, Asst. Prof. and Prof. A. R. Reddy, Prof. & Head, Dept. of ECE.

Resource Person: R Ashok Kumar Reddy, Chairman, Tadipatri Engineering College Founder (Alumni of Dept. of ECE, MITS)

Faculty attended:

Mr. K. Rahim Basha, Asst. Prof., ECE Dept.

Prof. A. R. Reddy, Prof., ECE Dept.

Dr. P. Ramanathan, Vice Principal (Academics)

Dr. P. Ramesh Reddy, Alumni Secretary, Asst. Prof., Math., Dept.

Students Attended: ECE: 45 Students

Venue: Scale-up room (ECE Dept.)

Mr. K. Rahim Basha Assistant Professor welcomed all the dignitaries, faculty and students and expressed his gratitude to the Management and Principal for giving permission and financial support to organize this programme.

Prof. A. R. Reddy, Professor and Head of the Electronics and Communication Engineering Department addressed the gathering. He briefly explained the motto of the programme.

Later, Dr. P. Ramanathan, Vice Principal (Academics), emphasized the importance of research scope in Electronics and Communication Engineering.

Next, Dr. P. Ramesh Reddy, Alumni Secretary, Asst. Professor, Department of Mathematics emphasized the importance of this invited talk and congratulated the organizers of the ‘Guest Lecture’ and conveyed his happiness and thanks to the participants.

1. To enhance basic knowledge of VLSI design and applications
2. To design a circuit along with geometrical designs for printing on silicon
3. To motivate the students towards emerging technologies
4. To discuss about various technologies related to VLSI and Embedded systems

The session is concluded by conveying vote of thanks by Dr. P. Ramesh Reddy, Alumni Secretary, Asst. Professor, Department of Mathematics.

