## MADANAPALLE INSTITUTE OF TECHMOLOGY & SCIENCE (UCG-AUTONOMOUS)

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Recognised Research Center, Accredited by NBA, NAAC for CSE, ECE, EEE, ME & MBA
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**Department of Electronics & Communication Engineering** 

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## Report on

Three-days online workshop on "Dynamic System Modelling and Signal Processing with MATLAB/ Simulink"

20.04.2021 to 22.04.21



Report Submitted by: Dr. Soumya Ranjan Mahapatro, Associate Professor, Dept. of ECE, Department of Electronics and Communication Engineering

No. of Participants – 60

Resource person: Mr. J prem Kumar, is presently working as a Product Manager for MathWorks Products in Capricot Technologies Private Limited and specializes in the field of Dynamic System Modelling, Embedded Systems, Robotics, Image Processing, Speech Processing, Deep Learning and Computer Vision. He has around 10 years of experience in Capricot Technologies. Prior to joining Capricot Technologies, he has experience in Semiconductor Manufacturing Industry. He holds a Master Degree in VLSI System Design.

The department of Electronics and Communication Engineering, MITS, Madanapallle organized a three days' online workshop program on **Dynamic System Modelling and Signal Processing with MATLAB/ Simulink during 20/Apr/2021 to 22/Apr/2021.** In this webinar **Mr. J prem Kumar** was enlighten with the fundamentals of MATLAB and Simulink. All session was very informative. More than one hundred students have registered for this workshop from different institutions across India.

	Day 1 session: Dynamic System Modelling with Simulink 20/04/2021
Topics	<ul> <li>Dynamic System Modelling with Simulink</li> <li>Digital System Design and HDL Code Generation</li> <li>Solvers and Auto Solver Feature</li> <li>Hardware Interface with Simulink</li> </ul>
Topics	Day 2 Session: MATLAB for Technical Computing 21/04/2021  • Analyzing and Visualizing Data with MATLAB • Graphics • Algorithm Development with MATLAB • App Building • Hardware Interface with MATLAB

## Day 3 session: Signal Processing with MATLAB & Simulink 22/04/2021

Generation of Signals Filter Design and Analysis

Filter Implementation HDL Code Generation

Topics



